

### **AMENDMENTS TO THE CLAIMS**

This listing of claims will replace all prior versions, and listings, of claims in the application.

1. (Withdrawn) A digital picture capturing device comprising:  
a plurality of pixel elements arranged in an array and adapted to capture picture information; and  
at least one encryption logic adapted to encrypt picture information from at least a first portion of the pixel elements with a first encryption key and to encrypt picture information from at least a second portion of the pixel elements with a second encryption key.
2. (Withdrawn) The digital picture capturing device according to claim 1, wherein:  
the first portion is a first single pixel element; and  
the second portion is a second single pixel element;  
wherein the encryption logic is adapted to encrypt each of the pixel elements with a different encryption key.
3. (Withdrawn) The digital picture capturing device according to claim 2, wherein:  
the encryption logic is a plurality of encryption logics; and  
each of the encryption logics connects to a respective one of the pixels.
4. (Withdrawn) The digital picture capturing device according to claim 3, wherein each of the plurality of encryption logics is located at a respective one of the pixels.

5. (Withdrawn) The digital picture capturing device according to claim 1, wherein the first portion is a first plurality of pixels and the second portion is a second plurality of pixels.

6. (Withdrawn) The digital picture capturing device according to claim 5, wherein the encryption logic is a single component that encrypts the first portion with the first encryption key and encrypts the second portion with the second encryption key.

7. (Withdrawn) The digital picture capturing device according to claim 6, further comprising:

pixel logic adapted to receive picture information from the first portion and the second portion and adapted to dispatch the picture information to the encryption logic.

8. (Withdrawn) The digital picture capturing device according to claim 1, wherein the array is a picture gathering device of a digital camera.

9. (Currently Amended) A digital picture display device comprising:  
a plurality of pixel elements arranged in an array and ~~adapted~~ each  
configured to illuminate to form a picture in response to a digital data stream representing  
digital display picture information; and  
decryption logic ~~adapted~~ configured to receive an encrypted data stream from  
a source device and to decrypt picture information from the encrypted data stream for at  
least a first portion of the plurality of pixel elements with a first decryption key and to  
decrypt picture information for at least a second portion of the plurality of pixel elements  
with a second decryption key.

10. (Previously Presented) The digital picture display device according to claim 9, wherein:  
the first portion is a first single pixel element; and  
the second portion is a second single pixel element;  
wherein the decryption logic is adapted to decrypt picture information for each of the pixel elements with a different decryption key.

11. (Currently Amended) The digital picture display device according to claim 10, wherein:  
each of the pixel elements includes decryption logic devices that connects to a respective one of the pixels.

12. (Previously Presented) The digital picture display device according to claim 11, wherein the decryption logic is positioned at a respective one of the pixels.

13. (Original) The digital picture display device according to claim 9, wherein the first portion is a first plurality of pixels and the second portion is a second plurality of pixels.

14. (Previously Presented) The digital picture display device according to claim 13, wherein the decryption logic is a single component that decrypts the first portion with the first decryption key and decrypts the second portion with the second decryption key.

15. (Previously Presented) The digital picture display device according to claim 14, further comprising:  
pixel logic adapted to receive decrypted picture information for the first portion from the decryption logic and to receive decrypted picture information for the second portion from the decryption logic and to dispatch the picture information for the first portion of pixel

elements to the decryption logic and to dispatch the picture information for the second portion to the second portion of pixel elements to the decryption logic.

16. (Original) The digital picture display device according to claim 9, wherein the array is a picture gathering device of a digital camera.

17. (Withdrawn) A method for encrypting information from an array, comprising:  
providing a plurality of pixels arranged in an array and adapted to capture picture information;  
partitioning the plurality of pixels into a plurality of portions;  
encrypting each of the plurality of portions with a respective one of a plurality of encryption keys, wherein each of the plurality of encryption keys is different than a remainder of the plurality of encryption keys.

18. (Withdrawn) The method according to claim 17, wherein each of the plurality of portions includes a plurality of pixels.

19. (Withdrawn) The method according to claim 17, wherein each of the plurality of portions includes only one pixel.

20.- 22. (Canceled)

23. (Currently Amended) A micro electronic device, comprising:  
an array of display pixels collectively configured to display visible images; and  
a plurality of decryption logic components, each decryption logic component associated with one or more ~~different group of the~~ display pixels and configured to decrypt video data directed to the respective group of display pixels.

24.-28. (Canceled)

29. (New) A system, comprising:
- a source device including:
    - a plurality of source pixels arranged into a plurality of regions, each region having at least one source pixel configured to capture digital picture information; and
    - encryption logic configured to encrypt the captured digital picture information in at least one of the regions using a plurality of encryption keys;
  - a receiving device in communication with the source device, the receiving device including:
    - a plurality of receiving pixels configured to illuminate to form a picture in response to the digital picture information; and
    - decryption logic configured to decrypt the encrypted digital picture information received from the source device using a plurality of decryption keys.
30. (New) The system of claim 29, wherein the source device is in communication with the receiving device through a network.
31. (New) The system of claim 30, wherein the source device is configured to transmit the encrypted digital picture information to the receiving device as an encrypted data stream across the network.
32. (New) The system of claim 30, wherein the network is one of a local area network, an Internet, an intranet, or a wireless link.
33. (New) The system of claim 29, wherein each region includes a single pixel and wherein each pixel includes encryption logic.
34. (New) The system of claim 29, wherein each receiving pixel includes decryption logic.

35. (New) The system of claim 29, wherein each source pixel includes a separate encryption logic embodied in a physical circuit located in the source pixel.

36. (New) The system of claim 29, wherein each source pixel includes a separate encryption logic embodied in a physical circuit that is connected to the source pixel.

37. (New) The system of claim 29, wherein each source pixel includes a separate encryption logic virtually connected to each of the source pixels through software programming in a communication device.

38. (New) The system of claim 29, wherein each receiving pixel includes a separate decryption logic embodied in a physical circuit located in the receiving pixel.

39. (New) The system of claim 29, wherein each receiving pixel includes a separate decryption logic embodied in a physical circuit that is connected to the receiving pixel.

40. (New) The system of claim 29, wherein each receiving pixel includes a separate decryption logic virtually connected to each of the receiving pixels through software programming in a communication device.

41. (New) The system of claim 37, wherein the communication device is a video card.

42. (New) The system of claim 29, wherein the encryption logic is physical circuitry fabricated in a semi-conductor substrate and wherein the pixel elements are microelectronic devices.

43. (New) The system of claim 29, wherein the source device includes source pixel logic configured to access pixel information from the source pixels.

44. (New) The system of claim 43, wherein the source pixel logic identifies each region of source pixels and transmits captured digital picture information from each of the regions to the encryption logic.

45. (New) The system of claim 31, wherein the receiving device includes receiving pixel logic configured to receive the encrypted data stream from the source device.

46. (New) The system of claim 45, wherein the receiving pixel logic is configured to format and addressably transmit the encrypted data stream received from the source device to respective receiving pixels in the receiving device.

47. (New) A method, comprising:  
    arranging a plurality of source pixels in a source device into a plurality of regions, each region having at least one source pixel configured to capture digital picture information; and  
    encrypting the captured digital picture information in at least one of the regions using a plurality of encryption keys;  
    arranging a plurality of receiving pixels in a receiving device wherein the receiving pixels are configured to illuminate to form a picture in response to the digital picture information;  
    transmitting the encrypted digital picture information to the receiving device as an encrypted data stream across a network; and  
    decrypting the encrypted digital picture information received from the source device using a plurality of decryption keys.